

## ANNAMACHARYA UNIVERSITY

### About Profile



NAME: M. HANUMANTHU

DATE OF BIRTH: 26-08-1984

DESIGNATION: Assistant Professor

DEPARTMENT: ECE

EMAIL ID: mht@aitsrajampet.ac.in

DATE OF JOINING:04.06.2011

EMPLOYEE ID: 075

### Academic Profile

Qualification	Name of the Board/University	YEAR
Ph.D.	North East Christian University, Nagaland	2025
M.Tech	Jawaharlal Nehru Technological University (JNTU), Ananthapuramu	2011
B. Tech	Jawaharlal Nehru Technological University (JNTU), Hyderabad	2005
Intermediate	Shiridi Sai junior College, Rajampet	2001
SSC	BVN School, Rajampet	1999

### Research Details

1. Areas of Specialization: Low Power VLSI Design.
2. List of Publications: See the table
3. Awards Received: Elsevier Reviewer
4. Research Guidance:

- A) No. of PhD Guided:  
 B) No. of M.Tech Guided: 9 batches  
 C) No. of B.Tech Guided: 32 batches

5. Details of Professional Membership:ISDS,IFERP,  
 IACSIT

6. Subjects Taught: DSP, ECA, EC, EDC, ADDC, DDTV, HDL, DCS, TT,  
 CMC, AVDA, PCB DESIGN, DD, DLD ETC.

### Publication Details

Title	Publisher	Published Year
Industrial Fault Detection System Using IoT Based Arduino and Node MCU	Social Science Research Network (SSRN)-Elsevier	2025
Hardware Efficient Realization of 128-bit Advanced Encryption Standard in FPGA	IEEE	2024
IoT Object Based Product Sorting Machine	IEEE	2024
Facemask Detection Using Bounding Box Algorithm Under COVID-19 Circumstances	SPRINGER	2023
Design of a Highly Reliable Low Power Stacked Inverter-Based SRAM Cell with Advanced Self-recoverability from Soft Errors	Springer	2023

### Patent Details

Sno.	Title of Patent	Submitted/Published/ Awarded
1	Self reliability based weighted softbit flipping algorithm for decoding EGLDPC Codes	Published