Faculty Profile

Basic Information:

NAME	:	**M SREENIVASULU**
DESIGNATION	:	**ASSISTANT PROFESSOR**
DEPARTMENT	:	**CSE**
DATE OF BIRTH	:	**27-06-1992**
DATE OF JOINING	:	**10-10-2022**
EMAIL ID	:	**meruvasreenivasulu.it@gmail.com**
EMPLOYEE ID:		**: 1667**

Academic Profile:

Qualification	Name of the Board/University	YEAR
Ph.D(Pursuing)	Annamacharya University	2024
M.Tech (CSE)	Jawaharlal Nehru Technological University(JNTUA) Anantapuramu	2016
B.Tech (IT)	Jawaharlal Nehru Technological University(JNTUA) Anantapuramu	2013

Research Details:

1. Areas of Specialization :		CSE
2. No. of Publications :		06
3. Awards Received :		-
4. Research Guidance		-
	No. of PhD Guided:	-
	No. of MTech. Guided:	-
	No. of B.Tech. Guided:	04
5. Details of Professional Membership:		-

6. Subjects Taught	:	C Programming ,Computer Organization , Design Patterns, Data Structures through Python, Computer Networks, Software Project Management, Data Structures ,HCI

Publication Details:

Title	Publisher	Published Year
Obtain Opinions from Online Reviews Based On theTrust	international journal of Advanced Research in Computer Science & Engineering	2016
A Two level intrusion detection system using BIO-INSPIRED APPROACH AND METAHEURISTIC SCALE	International Conference on Soft Computing for Problem Solving – ScoPros2018	2018
DITFEC: Drift Identification in Traffic-Flow Streams for DDoS Attack Defense Through Ensemble Classifier	Computing and Network Sustainability	2019
Fuzzy Deep Neural Network-Based Novel Feature Selection for Attack Detection in Big Data Environment	IJFANS INTERNATIONAL JOURNAL OF FOOD AND NUTRITIONAL SCIENCES	2022
Modeling IT ROLE in Shipping industry: AN APPLICATION OF ISM	INDUSTRIAL ENGINEERING JOURNAL	2023
LIGHTWEIGHT AND PRIVACY-PRESERVING DELEGATABLE PROOFS OF STORAGE WITH DATA DYNAMICS IN CLOUD STORAGE	international Conference AICCT 2024	2024

Patent Details:

Title of Patent	Submitted/Published/Awarded
Memory architecturing based on design of domain wall memory technology	Published