



ANNAMACHARYA UNIVERSITY

INTERNAL LAB EXAMINATION TIME TABLE

Attention: III B.Tech I Semester [2024 Admitted Batch Students]

Date: 18th October 2026

The Scheme of 2nd INTERNAL LAB EXAMINATION for III B.Tech I Semester Students is as indicated. The students are required to attend their respective laboratories on the day of the examination as per their class timetables.

DATE & DAY	CE	EEE – A	EEE – B	ME
22-10-2026 Thursday	Analysis and Design of structures Lab-I 10.05 a.m. To 12.35 p.m.	Power Electronics & Simulation Lab, Control Systems & Simulation Lab 01.30 p.m. To 04.00 p.m.	Power Systems lab 01.30 p.m. To 04.00 p.m.	Machining Processes Lab, I.C. Engines Lab 10.05 a.m. To 12.35 p.m.
02-11-2026 Monday	Transportation Engineering Lab, Geotechnical Engineering Lab 10.05 a.m. To 12.35 p.m.	Power Electronics & Simulation Lab, Control Systems & Simulation Lab 10.05 a.m. To 12.35 p.m.	Power Electronics & Simulation Lab, Control Systems & Simulation Lab 01.30 p.m. To 04.00 p.m.	Machining Processes Lab, I.C. Engines Lab 01.30 p.m. To 04.00 p.m.
03-11-2026 Tuesday		Power Systems lab 01.30 p.m. To 04.00 p.m.	Power Systems lab 10.05 a.m. To 12.35 p.m.	Refrigeration & Air Conditioning Lab 10.55 a.m. To 12.35 p.m.
04-11-2026 Wednesday	Analysis and Design of structures Lab-I 10.05 a.m. To 12.35 p.m. Transportation Engineering Lab, Geotechnical Engineering Lab 03.10 p.m. To 05.00 p.m.	Power Systems lab 10.05 a.m. To 12.35 p.m.	Power Electronics & Simulation Lab, Control Systems & Simulation Lab 10.05 a.m. To 12.35 p.m.	Refrigeration & Air Conditioning Lab 02.20 p.m. To 04.00 p.m.

CONTROLLER OF EXAMINATIONS

CHIEF CONTROLLER OF EXAMINATIONS

Attention: III B.Tech I Semester [2024 Admitted Batch Students]

Date: 18th October 2026

The Scheme of **2nd INTERNAL LAB EXAMINATION** for **III B.Tech I Semester** Students is as indicated. The students are required to attend their respective laboratories on the day of the examination as per their class timetables.

DATE & DAY	ECE – A	ECE – B	ECE – C	ECE – D
22-10-2026 Thursday		VLSI Design Lab, Microprocessors and Microcontrollers Lab 01.30 p.m. To 04.00 p.m.		
02-11-2026 Monday				VLSI Design Lab, Microprocessors and Microcontrollers Lab 10.05 a.m. To 12.35 p.m.
03-11-2026 Tuesday	VLSI Design Lab, Microprocessors and Microcontrollers Lab 01.30 p.m. To 04.00 p.m.	VLSI Design Lab, Microprocessors and Microcontrollers Lab 10.05 a.m. To 12.35 p.m.	VLSI Design Lab, Microprocessors and Microcontrollers Lab 04.00 p.m. To 05.30 p.m.	
04-11-2026 Wednesday	VLSI Design Lab, Microprocessors and Microcontrollers Lab 04.00 p.m. To 05.30 p.m.		VLSI Design Lab, Microprocessors and Microcontrollers Lab 10.05 a.m. To 12.35 p.m.	VLSI Design Lab, Microprocessors and Microcontrollers Lab 01.30 p.m. To 04.00 p.m.



CONTROLLER OF EXAMINATIONS



CHIEF CONTROLLER OF EXAMINATIONS

Attention: III B.Tech I Semester [2024 Admitted Batch Students]

Date: 18th October 2026

The Scheme of **2nd INTERNAL LAB EXAMINATION** for **III B.Tech I Semester** Students is as indicated. The students are required to attend their respective laboratories on the day of the examination as per their class timetables.

DATE & DAY	CSE – A	CSE – B	CSE – C	CSE – D	CSE – E	CSE – F	CSE – G
22-10-2026 Thursday				Artificial Intelligence Lab 09.15 a.m. To 10.55 p.m.			Computer Networks Lab 09.15 a.m. To 10.55 a.m.
02-11-2026 Monday		Artificial Intelligence Lab 09.15 a.m. To 10.55 p.m.	Computer Networks Lab 10.55 a.m. To 12.35 p.m.	Data Mining Lab 01.30 p.m. To 03.10 p.m.	Data Mining Lab 10.55 a.m. To 12.35 p.m.	Artificial Intelligence Lab 04.00 p.m. To 05.30 p.m. Venue:- New Lab (New Building East)	Data Mining Lab 03.10 p.m. To 04.50 p.m.
03-11-2026 Tuesday	Artificial Intelligence Lab 10.55 a.m. To 12.35 p.m.	Computer Networks Lab 04.00 p.m. To 05.30 p.m. Venue:- New Lab (New Building East)				Data Mining Lab 09.15 a.m. To 10.55 a.m.	
04-11-2026 Wednesday	Data Mining Lab 09.15 a.m. To 10.55 a.m. Venue:- EEE Lab (PG-Block)	Data Mining Lab 01.30 p.m. To 03.10 p.m.	Data Mining Lab 09.15 a.m. To 10.55 a.m. Venue:- Silicon Lab (Main-Building)	Computer Networks Lab 03.10 p.m. To 04.50 p.m. Venue:- Bell Lab (Main-Building)	Computer Networks Lab 09.15 a.m. To 10.55 a.m.	Computer Networks Lab 03.10 p.m. To 04.50 p.m.	Artificial Intelligence Lab 10.55 a.m. To 12.35 p.m. Venue:- JKC Lab (Main-Building)
	Computer Networks Lab 01.30 p.m. To 03.10 p.m. Venue:- EEE Lab (PG-Block)		Artificial Intelligence Lab 03.10 p.m. To 04.50 p.m.		Artificial Intelligence Lab 03.10 p.m. To 04.50 p.m. Venue:- JKC LAB (Main-Building)		


CONTROLLER OF EXAMINATIONS

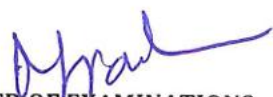

CHIEF CONTROLLER OF EXAMINATIONS

Attention: III B.Tech I Semester [2024 Admitted Batch Students]

Date: 18th October 2026

The Scheme of **2nd INTERNAL LAB EXAMINATION** for **III B.Tech I Semester** Students is as indicated. The students are required to attend their respective laboratories on the day of the examination as per their class timetables.

DATE & DAY	AI&DS – A	AI&DS – B	AI & DS – C	AI&ML – A	AI&ML – B	AI&ML – C
22-10-2026 Thursday		Data Mining Lab 10.55 a.m. To 12.35 p.m.	Automata Theory and Compiler Design Lab 09.15 a.m. To 10.55 a.m.		Data Wrangling and Exploratory Data Analysis Lab 10.55 a.m. To 12.35 p.m.	
02-11-2026 Monday		Automata Theory and Compiler Design Lab 01.30 p.m. To 03.10 p.m.		Computer Networks Lab 01.30 p.m. To 03.10 p.m.		Computer Networks Lab 04.00 p.m. To 05.30 p.m. Venue:- Civil Computer Lab (Civil-Block)
03-11-2026 Tuesday	Automata Theory and Compiler Design Lab 09.15 a.m. To 10.55 a.m.		Computer Networks Lab 01.30 p.m. To 03.10 p.m.		Computer Networks Lab 09.15 a.m. To 10.55 a.m.	Machine Learning Lab 01.30 p.m. To 03.10 p.m.
04-11-2026 Wednesday	Computer Networks Lab 09.15 a.m. To 10.55 a.m. Venue:- Civil Computer Lab (Civil-Block)	Computer Networks Lab 09.15 a.m. To 10.55 a.m. Venue:-Neumann Lab (Main-Building)	Data Mining Lab 03.10 p.m. To 04.50 p.m. Venue:- Civil Computer Lab (Civil-Block)	Machine Learning Lab 10.55 a.m. To 12.35 p.m.	Machine Learning Lab 01.30 p.m. To 03.10 p.m. Venue:-Neumann Lab (Main-Building)	Data Wrangling and Exploratory Data Analysis Lab 01.30 p.m. To 03.10 p.m. Venue:- Silicon Lab
	Data Mining Lab 03.10 p.m. To 04.50 p.m.			Data Wrangling and Exploratory Data Analysis Lab 03.10 p.m. To 04.50 p.m. Venue:- Neumann Lab (Main-Building)		


CONTROLLER OF EXAMINATIONS


CHIEF CONTROLLER OF EXAMINATIONS

Attention: III B.Tech I Semester [2024 Admitted Batch Students]

Date: 18th October 2026

The Scheme of **2nd INTERNAL LAB EXAMINATION** for **III B.Tech I Semester** Students is as indicated. The students are required to attend their respective laboratories on the day of the examination as per their class timetables.

DATE & DAY	CSE(AI) – A	CSE(AI) – B	CSE(AI) – C	CSE(AI) – D
22-10-2026 Thursday		Computer Networks Lab 03.10 p.m. To 04.50 p.m.		Deep Learning & Machine Learning Model Optimization Lab 03.10 p.m. To 04.50 p.m.
02-11-2026 Monday	Computer Networks Lab 04:00 p.m. To 05:30 p.m. Venue:- Placement Cell Lab Side- B (PG-Block)		Deep Learning & Machine Learning Model Optimization Lab 03.10 p.m. To 04.50 p.m.	
03-11-2026 Tuesday	Predictive Analytics Lab 03.10 p.m. To 04.50 p.m.	Deep Learning & Machine Learning Model Optimization Lab 04:00 p.m. To 05:30 p.m. Venue:- Placement Cell Lab Side- B (PG-Block)	Computer Networks Lab 04:00 p.m. To 05:30 p.m. Venue:- Bell Lab (Main- Building)	Computer Networks Lab 09.15 a.m. To 10.55 a.m.
04-11-2026 Wednesday	Deep Learning & Machine Learning Model Optimization Lab 01.30 p.m. To 03.10 p.m.	Predictive Analytics Lab 10.55 a.m. To 12.35 p.m.	Predictive Analytics Lab 10.55 a.m. To 12.35 p.m.	Predictive Analytics Lab 10.55 a.m. To 12.35 p.m. Venue:- Aryabhata Lab (New Building South)



CONTROLLER OF EXAMINATIONS



CHIEF CONTROLLER OF EXAMINATIONS

Attention: III B.Tech I Semester [2024 Admitted Batch Students]

Date: 18th October 2026

The Scheme of **2nd INTERNAL LAB EXAMINATION** for **III B.Tech I Semester** Students is as indicated. The students are required to attend their respective laboratories on the day of the examination as per their class timetables.

DATE & DAY	CSE(DS) – A	CSE(DS) – B	CSE(DS) – C
22-10-2026 Thursday	Data Mining Lab 03.10 p.m. To 04.50 p.m.	Computer Networks Lab 10.55 a.m. To 12.35 p.m.	Artificial Intelligence Lab 01.30 p.m. To 03.10 p.m.
02-11-2026 Monday	Artificial Intelligence Lab 09.15 a.m. To 10.55 a.m.	Data Mining Lab 04.00 p.m. To 05.30 p.m. Venue:- EEE Lab (PG-Block)	
03-11-2026 Tuesday			Computer Networks Lab 10.55 a.m. To 12.35 p.m.
04-11-2026 Wednesday	Computer Networks Lab 01.30 p.m. To 03.10 p.m.	Artificial Intelligence Lab 01.30 p.m. To 03.10 p.m. Venue:- Civil Computer Lab (Civil-Block)	Data Mining Lab 03.10 p.m. To 04.50 p.m. Venue:- Placement Cell Lab Side- A (PG-Block)


CONTROLLER OF EXAMINATIONS


CHIEF CONTROLLER OF EXAMINATIONS

Attention: III B.Tech I Semester [2024 Admitted Batch Students]

Date: 18th October 2026

The Scheme of **2nd INTERNAL LAB EXAMINATION** for **III B.Tech I Semester** Students is as indicated. The students are required to attend their respective laboratories on the day of the examination as per their class timetables.

DATE & DAY	CSE(AI&ML) – A	CSE(AI&ML) – B	CSE(AI&ML) – C	CSE(ICB) – A	CSE(ICB) – B
22-10-2026 Thursday	Computer Networks Lab 01.30 p.m. To 03.10 p.m.		Machine Learning Lab 10.55 a.m. To 12.35 p.m.	Internet of Things Lab 03.10 p.m. To 04.50 p.m.	Computer Networks Lab 01.30 p.m. To 03.10 p.m.
02-11-2026 Monday		Operating Systems Lab 03.10 p.m. To 04.50 p.m.		Computer Networks Lab 04:00 p.m. To 05.30 p.m. Venue:- Bell Lab (Main-Building)	
03-11-2026 Tuesday	Machine Learning Lab 03.10 p.m. To 04.50 p.m.	Computer Networks Lab 04.00 p.m. To 05.30 p.m. Venue:- Civil Computer Lab (Civil-Block)			Cloud Computing Lab 09.15 a.m. To 10.55 a.m.
04-11-2026 Wednesday	Operating Systems Lab 09.15 a.m. To 10.55 a.m. Venue:- Placement Cell Lab Side-A (PG-Block)	Machine Learning Lab 09.15 a.m. To 10.55 a.m.	Computer Networks Lab 09.15 a.m. To 10.55 a.m. Venue:- Placement Cell Lab Side-B (PG-Block)	Cloud Computing Lab 01.30 p.m. To 03.10 p.m. Venue:- IBM Lab (Main-Building)	Internet of Things Lab 09.15 a.m. To 10.55 a.m. Venue:- IBM Lab (Main-Building)
			Operating Systems Lab 03.10 p.m. To 04.50 p.m.		



CONTROLLER OF EXAMINATIONS



CHIEF CONTROLLER OF EXAMINATIONS