Faculty Profile

Basic Information:



NAME : **Naganjanamma Arkatavemula**

DESIGNATION : **Assistant Professor**

DEPARTMENT : **ECE**

DATE OF BIRTH : **26-06-1995**

DATE OF JOINING : **27-08-2026**

EMAIL ID : **Anjupmvu2695@gmail,com**

EMPLOYEE ID : 2288

Academic Profile:

Qualification	Name of the Board/University	YEAR
M.Tech	SRINIVASA INSTITUTE OF TECHNOLOGY AND SCIENCES, KADAPA	2026
B. Tech	SRI PADMAVATHI MAHILA VISWAVIDYALAYAM,TIRUPATH	2017

Research Details:

1. Areas of Specialization :		VLSI SYSTEM DESIGN
2. No. of Publications :		NIL
3. Awards Received :		Nil
4. Research Guidance		Nil
	No. of PhD Guided:	Nil
	No. of MTech. Guided:	Nil
	No. of B.Tech. Guided:	Nil
5. Details of Professional Membership:		Nil
6. Subjects Taught :		---

Publication Details:

Title	Publisher	Published Year
Design of Parallel MAC unit for FPGA based Deep learning Algorithms using VHDL" (Under Review)	IEEE	--
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Patent Details:

Title of Patent	Submitted/Published/Awarded
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