SDG 9: Industry, Innovation and Infrastructure

Annual Report 2024–25

Annamacharya University, Rajampet, is committed to advancing quality education through industry engagement, technology-enabled learning, research, innovation and the development of infrastructure that responds to the needs of society and the economy. The University's institutional vision emphasises becoming a centre of excellence through world-class infrastructure, industrial tie-ups, research and innovation, international collaboration and outreach.

Institutional Commitment and Enabling Mechanisms

The University's public institutional profile identifies industry and society as important reference points for its academic programmes. Its stated development priorities include modern infrastructure, industry partnerships, international academic collaboration, research and innovation, and outreach activities.

The University's research and academic structure provides institutional channels relevant to SDG 9, including a Directorate of Research, Research and Development Cell, University Research Board, central research laboratories and facilities, collaboration and MoU mechanisms, innovation and incubation activities, and an Entrepreneurship Development Cell. These mechanisms provide a framework for connecting teaching and learning with applied research, technology development and external expertise.

The University also identifies industry-collaboration laboratories in technology-related departments. The published departmental information describes the use of industry partnerships, including Siemens-related collaboration, to provide practice-oriented exposure and strengthen students' familiarity with current technologies.

List of Activities Documented During 2024–25

Initiative / activity	Date / period	Relevance
Strategic MoU with Kaptius India Private Limited	1 July 2024	Industry–academia partnership; technology integration; cloud/ServiceNow skills; entrepreneurship and employability.
Three-day hands-on workshop on AI, Machine Learning and Data Science	29–31 August 2024	Practical digital technology skills and applied AI/data science exposure.

Initiative / activity	Date / period	Relevance
PCB Design and Prototype Development workshop	3–6 September 2024	Electronics design, prototyping and practical engineering capability.
Guest lecture on 3S Prefabricated Building Systems	2024 (as reported in University newsletter)	Exposure to modern construction methods and infrastructure technologies.
Hybrid Energy Storage System for DC Micro Grid	Listed in 2024 University events archive	Energy-system innovation and engineering knowledge relevant to resilient infrastructure.

Strategic Industry Partnership with Kaptius

- **Activity title:** MoU with Kaptius India Private Limited
- **Date:** 1 July 2024
- **Partner:** Kaptius India Private Limited
- **Location:** Annamacharya University, Rajampet

Annamacharya University formalised a strategic partnership with Kaptius India Private Limited to strengthen the connection between higher education and the technology-enabled workplace. The published University report explains that the collaboration is intended to integrate contemporary technology into the curriculum, particularly in management, software and technological solutions used by multinational corporations.

During the MoU signing programme, the Kaptius representative, Havish Kolchina, Head of Service Delivery at Kaptius Hyderabad, outlined areas in which the company could contribute expertise, including startup support, pre-placement training, advisory services, go-to-market strategies, social and digital marketing, and investment fundraising. The University's Registrar highlighted the value of the partnership in strengthening students' technological competencies and employability.

The partnership is relevant to SDG 9 because it creates a formal channel for industry knowledge to inform academic learning and capability development. Its focus on cloud-based organisations and ServiceNow-related workflows connects students with contemporary digital infrastructure and enterprise technology. The stated support areas for startups and entrepreneurship also connect technological capability with innovation and enterprise development.

- A strategic industry partnership was formalised and publicly reported by the University.
- The collaboration established a framework for technology-oriented curriculum enrichment and industry interaction.

- The partner identified potential support areas in pre-placement preparation, startup support, advisory services and business development.
- The initiative strengthened the University's institutional approach to employability, innovation and industry-linked learning.

The University's official event page contains photographs associated with the MoU programme. The links below open the original University-published photographs and event record. For final institutional publication, the University may download and insert the original-resolution photographs from the official page, retaining the source attribution.

Official event page: MoU with Kaptius – Annamacharya University



Photograph 1 – University-published event photograph

Photograph gallery – additional images: View photographs on the official event page

AI, Machine Learning and Data Science Workshop

A three-day hands-on workshop titled "A Comprehensive Introduction to AI/Machine Learning/Data Science" was conducted from 29 to 31 August 2024 for second-year students of Artificial Intelligence and Data Science and Computer Science Engineering (Data Science). The University newsletter records that the programme included practical sessions using AI tools and explored applications such as plant-health prediction, weather reporting, digital information management and banking applications.

The programme involved guest lecturers from SKYED Technologies, Visakhapatnam, including Mrs. Sneha Latha Reddy and Mr. Dinesh. The newsletter identifies Dr. P. Phanindra Kumar Reddy, Head of the Department of AI & DS, as the coordinator, with support from programme coordinator Mr. B. Venkatesh Goud.



Evidence: Official University newsletter (July–October 2024)

PCB Design and Prototype Development

The University's 2024 events archive lists a workshop on "PCB Design and Prototype Development" conducted from 3 to 6 September 2024. The activity is relevant to SDG 9 through its focus on electronic product design and prototyping, which are important stages in engineering innovation and technology development.



Evidence: Official event record

Guest Lecture on 3S Prefabricated Building Systems

The University newsletter reports a Civil Engineering Department guest lecture on 3S Prefabricated Building Systems. The session was delivered by J. Kranti Kumar Reddy, Executive Engineer, CPWD, and explained the application of prefabricated building systems through a presentation and video demonstration. The activity exposed students to an alternative construction approach and contemporary infrastructure practices.



Evidence: Official University newsletter (July–October 2024)

Institutional Infrastructure and Research Ecosystem

The University's official website identifies a campus extending over 100 acres and describes its commitment to developing world-class infrastructure and facilities. Its published academic and research structure includes engineering disciplines, central research laboratories and facilities, research and development mechanisms, collaboration channels and innovation/incubation functions.

The University's public departmental information also describes industry collaboration laboratories in Artificial Intelligence and Machine Learning and Computer Science (Artificial Intelligence), including Siemens-related collaboration. These facilities are presented as supporting hands-on experience, industry interaction, workshops and short-term technical programmes.





These institutional mechanisms provide the enabling environment for SDG 9 by supporting technological learning, applied research, engineering education and collaboration with external organisations.

Focus area	Institutional contribution documented
Industry collaboration	The Kaptius partnership established a formal channel for industry interaction and technology-oriented learning.
Innovation and digital capability	The AI/ML/Data Science workshop offered hands-on exposure to contemporary digital tools and applications.
Engineering design and prototyping	The PCB design and prototype development workshop addressed practical electronics design skills.
Infrastructure knowledge	The 3S prefabricated building systems lecture exposed Civil Engineering students to modern construction practices.
Research and technology ecosystem	Research, laboratory, collaboration and innovation structures provide institutional support for technology development.

During Academic Year 2024–2025, Annamacharya University documented initiatives that connect engineering and technology education with industry engagement, digital skills, prototyping and infrastructure-related knowledge. The Kaptius partnership, hands-on AI/ML/Data Science workshop, PCB design and prototype development programme, and Civil Engineering lecture on prefabricated building systems demonstrate the University's engagement with themes central to SDG 9.

This report consolidates the University's publicly documented SDG 9-related initiatives into an institutional progress publication. It is intended to support transparent communication of the University's contribution to industry, innovation and infrastructure and to serve as supporting evidence for THE Impact Rankings Indicator 17.3.9.

10. References and Official Evidence Sources

Annamacharya University – About the University

Annamacharya University – Research

Annamacharya University – MoU with Kaptius (event date 01.07.2024)

Annamacharya University – Events 2024 archive

Annamacharya University – Newsletter, July–October 2024

Annamacharya University – PCB Design and Prototype Development workshop

Annamacharya University – AIML Industry Collaboration Labs

Annamacharya University – CSE (AI) Industry Collaboration Labs

Annamacharya University - Industry Collaboration Labs

Industry-Linked Technical Internships – Academic Year 2024–25

During Academic Year 2024–2025, students of the Electrical and Electronics Engineering (EEE) programme completed short-term technical internships in specialised areas of electrical and electronics engineering. The submitted completion certificates document five students completing a two-month virtual internship in VLSI Design with Verilog through Technotran Company and one student completing a two-month internship in Substation Design through SkillDzire, in collaboration with the Andhra Pradesh State Council of Higher Education (APSCHE).

These internships provided focused exposure to semiconductor design and digital hardware programming, as well as electrical power-system infrastructure. The VLSI programme specifically covered Verilog-based design, while the Substation Design internship addressed a core area of electrical power infrastructure. Such structured external training complements classroom learning and supports practical technical capability, industry-oriented skills and technology readiness.

Internship details supported by completion certificates

S. No.	Student name	Programme / Roll No.	Internship title	Organisation / partner	Duration and period / issue date	Certificate ID
1	Ganga Geethika	EEE / 21701A0236	VLSI Design with Verilog	Technotran Company	2 months; 1 June–31 July 2024	TT-ICP-241572
2	Palepu Jahnavi	EEE / 21701A0250	VLSI Design with Verilog	Technotran Company	2 months; 1 June–31 July 2024	TT-ICP-241573
3	Ayushi Jain	EEE / 21701A0211	VLSI Design with Verilog	Technotran Company	2 months; 1 June–31 July 2024	TT-ICP-241574
4	Haritha	EEE / 21701A0245	VLSI Design with Verilog	Technotran Company	2 months; 1 June–31 July 2024	TT-ICP-241575
5	Nandyala Anusharma	EEE / 21701A0207	VLSI Design with Verilog	Technotran Company	2 months; 1 June–31 July 2024	TT-ICP-241576

S. No.	Student name	Programme / Roll No.	Internship title	Organisation / partner	Duration and period / issue date	Certificate ID
6	Ampvatina Veerapranavi	EEE / 23705A0235	Substation Design	SkillDzire, in collaboration with APSCHE	2 months; certificate issued 23 November 2024	SDST-22993

The five Technotran certificates record the same two-month virtual programme from 1 June 2024 to 31 July 2024, with individual certificate IDs. The SkillDzire certificate confirms completion of a two-month Substation Design internship and records the issue date as 23 November 2024; the certificate does not specify the exact internship start and end dates.

Contribution to industry, innovation and infrastructure: The VLSI Design with Verilog internships strengthened exposure to digital electronics and semiconductor-oriented design skills. The Substation Design internship contributed exposure to electrical power infrastructure. Together, these six certified internships demonstrate student participation in specialised, externally delivered technical training during the reporting year and add individual-level evidence to the University's broader industry-linked learning and technology-development initiatives.

Annexure 11A – Internship completion certificates

Certificate 1: Ampvatina Veerapranavi



SkillDzire

CERTIFICATE OF INTERNSHIP

This is to Certify that Mr./Ms

Ampvatina Veerapranavi

Enrolled in the Electrical and Electronics Engineering - 23705A0235

From College Annamacharya Institute of Technology & Sciences

of university JNTUA, Anantapur

has Successfully Completed short-term Internship programme titled

Substation Design

Under SkillDzire for 2 months.Organized By **SkillDzire** in collaboration with **Andhra Pradesh State Council of Higher Education**.

Certificate ID: **SDST-22993**

Issued On **23-November-2024**

Approved By AICTE

Authorized Signature

Certificate 2: Nandyala Anusharma


AICTE Internship ID :INTERNSHIP_1713271269661e71e536db2

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INTERNSHIP

COMPLETION CERTIFICATE

THIS IS TO CERTIFY THAT

Nandyala Anusharma

student of the EEE department, Annamacharya Institute Of Technology & Science, Rajampet, has Successfully Completed the 2-month Virtual Internship Certification Program at Technotran Company on VLSI Design With Verilog from 1st June 2024 to 31st July 2024 .

Certificate ID No: TT-ICP-241576
Roll No: 21701A0207

R.Vikas Reddy
Director,
Technotran

To verify the authenticity of this certificate, please email us at admin@technotran.in

www.technotran.in

Certificate 3: Haritha


AICTE Internship ID :INTERNSHIP_1713271269661e71e536db2

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INTERNSHIP

COMPLETION CERTIFICATE

THIS IS TO CERTIFY THAT

Haritha

student of the EEE department, Annamacharya Institute Of Technology & Science, Rajampet, has Successfully Completed the 2-month Virtual Internship Certification Program at Technotran Company on VLSI Design With Verilog from 1st June 2024 to 31st July 2024 .

Certificate ID No: TT-ICP-241575
Roll No: 21701A0245

R.Vikas Reddy
Director,
Technotran

To verify the authenticity of this certificate, please email us at admin@technotran.in

www.technotran.in

Certificate 4: Ayushi Jain



Certificate 5: Palepu Jahnavi



Certificate 6: Ganga Geethika



AICTE Internship ID :INTERNSHIP_1713271269661e71e536db2

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COMPLETION CERTIFICATE

THIS IS TO CERTIFY THAT

Ganga Geethika

student of the EEE department, Annamacharya Institute Of Technology & Science, Rajampet, has Successfully Completed the 2-month Virtual Internship Certification Program at Technotran Company on VLSI Design With Verilog from 1st June 2024 to 31st July 2024 .

Certificate ID No: TT-ICP-241572
Roll No: 21701A0236

R.Vikas Reddy
Director,
Technotran

To verify the authenticity of this certificate, please email us at admin@technotran.in

www.technotran.in