Faculty Profile

Basic Information:



NAME	:	**Dr.KOMMURI HARINATH REDDY**
DESIGNATION	:	**ASSISTANT PROFESSOR**
DEPARTMENT:		**EEE**
DATE OF BIRTH	:	**28/081981**
DATE OF JOINING	:	**18/09/2008**
EMAIL ID	:	**harinathreddyks@gmail.com**
EMPLOYEE ID:		**:12**

Academic Profile:

Qualification	Name of the Board/University	YEAR
Ph.D	MONAD UNIVERSITY,UP	2024
M.Tech	JNTUA	2010
B.Tech	SKU,ANANTAPUR	2006

Research Details:

1. Areas of Specialization	:	EMBEDDED SYSTEMS
2. No. of Publications	:	30
3. Awards Received	:	NIL
4. Research Guidance		
	No. of PhD Guided:	
	No. of MTech. Guided:	12
	No. of B.Tech. Guided:	20
5. Details of Professional Membership:		ISTE & IE
6. Subjects Taught	:	STLD, ELECTRICAL MACHINES, ANALOG ELECTRONICS, INSTRUMENTATION, EHVAC/DC, MPMC, ESPS, BEEE.

Publication Details:

Title	Publisher	Published Year
1.Economic Load Dispatch Downside with Valve-Point Result Employing a Binary Bat formula	International Journal of Electrical and Computer Engineering	2014
2.Optimal location and size of distributed generations using kalman filter algorithm for reduction of power loss and voltage profile improvement	International Journal of Engineering Research and Development	2014
3.Economic Load Dispatch Problem with Valve-Point Effect Using a Binary Bat Algorithm	ACEEE Int. J. on Electrical and Power Engineering	2013
4.Modeling and Simulation of SRF Control Based Shunt Active Power Filter and Application to BLDC Drive	International Journal of Computer Science and Information Security	2014
5.Back Propagation Algorithm Based Fuzzy Logic Strategy for Wind-DG Microgrid	Pramana Research Journal	2017
6.LOW FREQUENCY OSCILLATIONS DAMPED BY USING D-FACTS CONTROLLER	International Journal of Computer Science and Information Security	2014
7.Distributed static series compensators in mitigating sub-synchronous resonance improved with ANFIS controller	International Conference on Electrical, Electronics, Signals, Communication and Optimization (EESCO)	2015
8.Power quality enhancement in microgrid using artificial intellegence controller	2nd International Conference on Communication and Electronics Systems	2017
9.Design of PI speed controller for 3-Ph converter fed DC motor drive using symmetrical optimization	Journal of Applied Science and Engineering	2021
10.Water Cycle Algorithm Tuned Cascade Controller for the Frequency Control of a Nonlinear Hydrothermal Power System with PEV Integration	E3S Web of Conferences	2024
11.Subsynchronous Resonance Mitigation in SMIB System Using GCSC and TCSC with Conventional and Fuzzy Logic Controllers	Third International Conference on Advances in Electrical, Computing	2023
12.An Efficient Shortest Path Optimized Algorithm For Vehicle Adhoc Networks System	2024 7th International Conference on Circuit Power and Computing Technologies	2024
13.Hybrid boost-cuk converter with bat-chicken swarm-optimized PI controller for photovoltaic grid systems	Future Technology	2025