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Rajampet, AP-516126, India
School of Electrical & Electronics Enginering

Guest Lectures by Alumni

S. No.	Name of the Alumni and Batch	Designation & Organization	Name of the Topic	Date & Mode of the Guest Lecture	Sustainable Development Goal	No. of Students Attended
1.	Mr. K. Sreekanth (2003-2007)	Site Manager, Energy and Power Contracting Company, Kingdom of Saudi Arabia	Operation of Gas Insulated Substations	10/07/2026 (Offline)	SDG 9 Industry, Innovation and Infrastructure	IV EEE 74
2.	Mr. B. Poli Reddy (2019-2023)	Application Engineer, Amara Raja Energy Mobility Ltd. Tirupati	Engineering Energy: Production Process of Lead Acid Batteries	28/02/2026 (Offline)	SDG 9 Industry, Innovation and Infrastructure	III & IV EEE 68
3.	Mr. C. Tirupati Balaji (2019-2023)	Semiconductor Validation Engineer, UST, Bangalore.	Post-Silicon Validation: Bridging Design and Real-World Performance	21/02/2026 (Offline)	SDG 9 Industry, Innovation and Infrastructure	III EEE 63
4.	Mr. V. Harish (2014-2018)	Technical Lead, HCL Technologies, Hyderabad.	Introduction to VLSI: Bridging Academia and Semiconductor Industry	07/02/2026 (Offline)	SDG 9 Industry, Innovation and Infrastructure	III & IV EEE 55
5.	Mrs. P. Nagamani (2014-2018)	R & D Engineer - ABB Global Industries & Services Pvt. Ltd., Hyderabad.	Exploring Career Pathways in Core Engineering Sectors	31/01/2026 (Online)	SDG 8 Decent Work and Economic Growth	III EEE 51
6.	Mr. B. Subbarayudu (2014-2018)	Senior Software Engineer, Capgemini Hyderabad.	.NET: Skills, Careers, and Real-World Applications	24/01/2026 (Offline)	SDG 8 Decent Work and Economic Growth	III EEE 52
7.	Mr. M. Muni Kumar (2014-2018)	Senior Technical Associate, Evoke, Hyderabad.	Microsoft Azure: Cloud Computing, Career Opportunities	03/01/2026 (Online)	SDG 8 Decent Work and Economic Growth	III EEE 69

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S. No.	Name of the Alumni and Batch	Designation & Organization	Name of the Topic	Date & Mode of the Guest Lecture	Sustainable Development Goal	No. of Students Attended
8.	Mr. K. Kupendra Achari (2014-2018)	Mr. K. Kupendra Achari, Advanced software engineer, Aptiv Software Components Pvt. Ltd., Bengaluru.	Career Launchpad: Job Search Mastery and Trending Technologies	27/12/2025 (Online)	SDG 8 Decent Work and Economic Growth	II EEE 71
9.	Mr. A. Leelendra Prasad (2019-2023)	M. Tech (Power Engineering), IIT Kanpur.	Beyond the GATE: Unlocking Your Engineering Future	20/12/2025 (Offline)	SDG 4 Quality Education	III EEE 53
10.	Mr. P. Sreenivasula Reddy (2015-2019)	Mr. P. Sreenivasula Reddy, Social Security Officer, Ministry of Labour and Employment, Govt. of India, Chennai.	Career Horizons: Exploring the Avenues for Graduates	11/10/2025 (Offline)	SDG 8 Decent Work and Economic Growth	III & IV EEE 100

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Report on Guest Lecture by Mr. K. Sreekanth



A guest lecture titled “Operation of Gas Insulated Substations” was organized on 10/07/2026 for the students of IV B.Tech, Department of Electrical and Electronics Engineering. The session was delivered by Mr. K. Sreekanth (2003–2007 Batch EEE Alumni), currently working as a Site Manager at Energy and Power Contracting Company, Kingdom of Saudi Arabia. The objective of the lecture was to provide students with practical knowledge of the construction, operation, maintenance, protection, and safety aspects of Gas Insulated Substations (GIS) used in modern power transmission and distribution systems.

During the session, the speaker introduced students to the basic concept and importance of Gas Insulated Substations in high-voltage power systems. He explained the major components of GIS, including gas-insulated busbars, circuit breakers, disconnectors, earthing switches, current transformers, voltage transformers, and cable terminations. The speaker highlighted the compact construction of GIS and its suitability for substations where space limitations, environmental conditions, and reliability requirements are important considerations.

Mr. K. Sreekanth explained the operating principles of GIS, with particular emphasis on the role of insulating gas in providing electrical insulation and arc-quenching capability. He discussed the sequence of operation of circuit breakers, isolators, and earthing switches during normal switching and fault conditions. The speaker also explained the importance of interlocking arrangements and safe operating procedures to prevent equipment damage and ensure personnel safety.

The session further covered the protection and control aspects of GIS, including fault detection, circuit-breaker operation, protection coordination, and monitoring systems. The speaker shared practical insights into the commissioning and operation of GIS installations and explained the importance of systematic inspection, preventive maintenance, gas monitoring, leakage detection, and testing procedures for ensuring reliable substation operation.

Drawing from his professional experience in the Kingdom of Saudi Arabia, the speaker discussed various field-level challenges encountered during the installation, commissioning, and operation of high-voltage substations. He emphasized the importance of following electrical safety standards, permit-to-work procedures, isolation and earthing practices, personal protective equipment, and maintenance protocols while working in high-voltage environments. These practical examples helped students understand the difference between theoretical concepts and actual industrial practices.

The speaker also highlighted the increasing application of GIS technology in modern electrical infrastructure due to its compact size, high reliability, reduced maintenance requirements, and suitability for urban and high-voltage applications. He explained the career opportunities available to Electrical and Electronics Engineering graduates in substation design, testing, commissioning, operation, maintenance, protection, and power-system projects.

The lecture was aligned with Sustainable Development Goal (SDG) 9 – Industry, Innovation and Infrastructure, as GIS technology supports the development of modern, reliable, efficient, and resilient electrical infrastructure. The session enabled students to understand how advanced substation technologies contribute to the modernization of power transmission and distribution networks.

The session was highly interactive, with students actively participating in discussions and raising questions related to GIS operation, circuit-breaker switching, insulation systems, protection mechanisms, maintenance practices, safety procedures, commissioning, and career opportunities in the power sector. The speaker addressed the queries with practical examples based on his extensive industrial experience.

Overall, the guest lecture was highly informative and beneficial to the students of IV EEE. It provided valuable industry-oriented knowledge on the operation, protection, maintenance, safety, and applications of Gas Insulated Substations. The Department of Electrical and Electronics Engineering expressed its sincere appreciation to Mr. K. Sreekanth for sharing his professional expertise and practical experience. Such alumni-industry interactions significantly enhance students' practical understanding, industry readiness, awareness of modern power-system technologies, and professional competencies while strengthening alumni engagement and supporting institutional quality initiatives.

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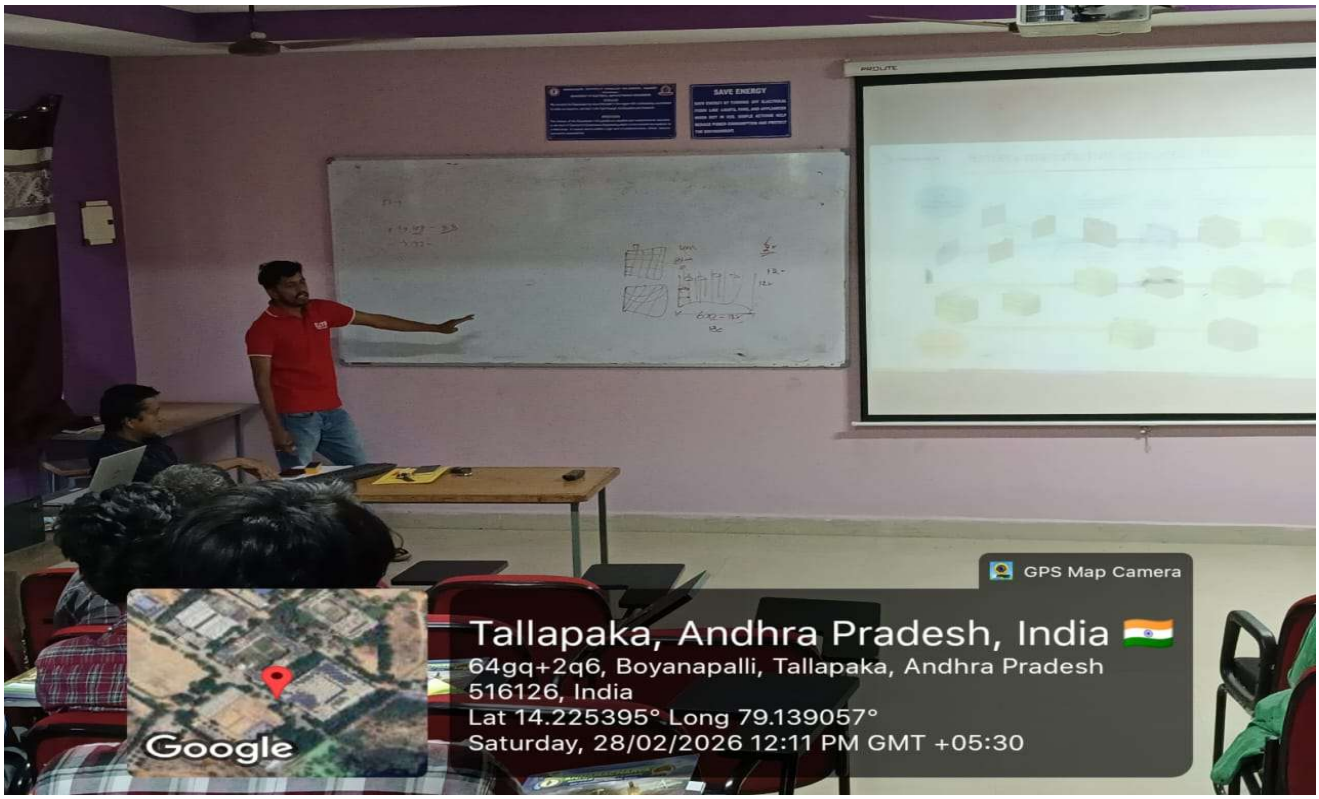
Report on Guest Lecture by Mr. B. Poli Reddy



Guest Lecture on "Engineering Energy: Production Process of Lead Acid Batteries"
by Mr. B. Poli Reddy

A guest lecture titled "Engineering Energy: Production Process of Lead Acid Batteries" was organized on 28/02/2026 for the students of III and IV B. Tech, Department of Electrical and Electronics Engineering. The session was delivered by Mr. B. Poli Reddy (2019–2023 Batch EEE Alumni), currently working as an Application Engineer at Amara Raja Energy Mobility Ltd., Tirupati. The objective of the lecture was to provide students with practical knowledge of battery manufacturing processes, energy storage technologies, and industrial applications of lead-acid batteries in the modern energy sector.

During the session, the speaker introduced students to the importance of energy storage systems and the significant role played by lead-acid batteries in automotive, industrial, renewable energy, and backup power applications. He explained the basic construction and working principle of lead-acid batteries, including the functions of positive and negative plates, separators, electrolyte, and battery containers. The speaker also highlighted the advantages, limitations, and performance characteristics of lead-acid battery technology.

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Mr. B. Poli Reddy provided a detailed overview of the battery production process followed in the industry. He explained various stages such as grid manufacturing, paste mixing, plate pasting, curing, assembly, electrolyte filling, charging, testing, and quality control procedures. The speaker emphasized the importance of maintaining manufacturing standards, safety practices, and quality assurance measures to ensure reliable battery performance and long service life.

The speaker further discussed the growing demand for energy storage systems due to the increasing adoption of electric vehicles, renewable energy integration, and uninterrupted power supply requirements. He shared insights into industrial practices at Amara Raja Energy Mobility Ltd. and explained the technological advancements being implemented to improve battery efficiency, durability, and environmental sustainability. He also highlighted the career opportunities available for electrical engineering graduates in battery manufacturing, energy storage, electric mobility, and related sectors.

In addition, the session addressed the relevance of Sustainable Development Goal (SDG) 9 – Industry, Innovation and Infrastructure by showcasing how advanced battery manufacturing technologies contribute to industrial growth, innovation, and sustainable infrastructure development. The speaker encouraged students to develop strong technical skills and stay updated with emerging trends in energy storage and electric mobility technologies.

The lecture was highly interactive, with students actively participating in discussions and raising questions related to battery manufacturing, testing procedures, industrial requirements, research opportunities, and career prospects. The speaker answered these queries with practical examples drawn from his professional experience, providing students with valuable industry-oriented insights.

Overall, the guest lecture was highly informative and beneficial for the students. It provided 68 students of III and IV EEE with a comprehensive understanding of the production process of lead-acid batteries, industrial manufacturing practices, and emerging opportunities in the energy storage sector. The Department of Electrical and Electronics Engineering expressed its sincere appreciation to the alumnus for sharing his expertise and industry experience. Such alumni interactions significantly enhance students' practical knowledge, industry readiness, and awareness of technological advancements while strengthening alumni engagement and supporting institutional quality initiatives.

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Report on Guest Lecture by Mr. C. Tirupati Balaji



Guest Lecture on "Post-Silicon Validation: Bridging Design and Real-World Performance" by Mr. C. Tirupati Balaji

A guest lecture titled "Post-Silicon Validation: Bridging Design and Real-World Performance" was organized on 21/02/2026 for the students of III B. Tech, Department of Electrical and Electronics Engineering. The session was delivered by Mr. C. Tirupati Balaji (2019–2023 Batch EEE Alumni), currently working as a Semiconductor Validation Engineer at UST, Bangalore. The objective of the lecture was to provide students with an understanding of semiconductor product development, post-silicon validation processes, and the critical role of validation engineers in ensuring the performance and reliability of semiconductor devices.

During the session, the speaker introduced students to the semiconductor industry and explained the various stages involved in the design and development of integrated circuits (ICs). He highlighted the transition from chip design and fabrication to validation and testing, emphasizing the importance of post-silicon validation in verifying whether a manufactured chip performs according to its design specifications under real-world operating conditions.

Mr. C. Tirupati Balaji provided a detailed explanation of post-silicon validation techniques, including functional testing, performance evaluation, debugging, characterization, and reliability assessment. He discussed how validation engineers identify and resolve hardware and software issues that may arise after fabrication, ensuring that semiconductor products meet quality, performance, and safety standards before deployment in commercial applications.

The speaker also explained the tools, methodologies, and test environments commonly used in semiconductor validation. He highlighted the significance of data analysis, problem-solving skills, hardware-software interaction, and teamwork in the validation process. Through practical examples from his professional experience, he demonstrated how validation activities contribute to the successful launch of semiconductor products used in consumer electronics, communication systems, automotive technologies, and industrial applications.

In addition, the speaker discussed emerging trends in the semiconductor sector, including artificial intelligence hardware, Internet of Things (IoT) devices, high-performance computing, and advanced chip technologies. He emphasized the growing demand for skilled engineers in semiconductor design, testing, and validation domains. The session also highlighted the relevance of Sustainable Development Goal (SDG) 9 – Industry, Innovation and Infrastructure, as semiconductor technologies form the foundation for innovation, industrial growth, and modern technological infrastructure.

Mr. Tirupati Balaji further guided students on career opportunities available in the semiconductor industry and suggested ways to prepare for such roles by strengthening their fundamentals in electronics, digital systems, embedded systems, programming, and data analysis. He encouraged students to pursue internships, industry certifications, and practical projects to enhance their employability in the rapidly evolving semiconductor sector.

The lecture was highly interactive, with students actively engaging in discussions and seeking clarification on semiconductor manufacturing, validation workflows, industry expectations, and career pathways. The speaker addressed these queries with practical insights and real-world examples, helping students gain a deeper understanding of the field.

Overall, the guest lecture was highly informative and inspiring. It provided 63 students of III EEE with valuable knowledge about post-silicon validation, semiconductor product development, and emerging opportunities in the semiconductor industry. The Department of Electrical and Electronics Engineering expressed

its sincere appreciation to the alumnus for sharing his expertise and professional experiences. Such alumni interactions play a significant role in enhancing industry awareness, technical competence, and career preparedness among students while strengthening alumni engagement and supporting institutional quality initiatives.

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Report on Guest Lecture by Mr. V. Harish



Guest Lecture on "Introduction to VLSI: Bridging Academia and Semiconductor Industry" by Mr. V. Harish

A guest lecture titled "Introduction to VLSI: Bridging Academia and Semiconductor Industry" was organized on 07/02/2026 for the students of III and IV B. Tech, Department of Electrical and Electronics Engineering. The session was delivered by Mr. V. Harish (2014–2018 Batch EEE Alumni), currently working as a Technical Lead at HCL Technologies, Hyderabad. The objective of the lecture was to introduce students to the fundamentals of Very Large-Scale Integration (VLSI), provide insights into the semiconductor industry, and bridge the gap between academic learning and industrial requirements.

During the session, the speaker provided an overview of VLSI technology and its significance in modern electronic systems. He explained the evolution of integrated circuits, semiconductor devices, and chip design technologies that form the backbone of today's computing, communication, consumer electronics, automotive, and industrial automation sectors. The speaker emphasized the growing importance of VLSI in enabling compact, high-performance, and energy-efficient electronic products.

Mr. V. Harish discussed the various stages involved in the VLSI design flow, including specification, design entry, simulation, synthesis, verification, physical design, testing, and fabrication. He explained the roles of front-end and back-end design engineers and highlighted the importance of digital electronics, semiconductor devices, and programming skills for pursuing a career in the VLSI domain. The speaker also introduced students to commonly used industry tools and design methodologies adopted in semiconductor companies.

The lecture focused on bridging the gap between academia and industry by explaining the technical competencies expected from engineering graduates entering the semiconductor sector. He emphasized the need for a strong foundation in electronics fundamentals, logical thinking, problem-solving abilities, and continuous learning to adapt to emerging technologies. Through examples from his professional experience, he illustrated how academic concepts are applied in real-world chip design and verification projects.

In addition, the speaker discussed the rapid growth of the semiconductor industry and emerging opportunities in areas such as chip design, verification, embedded systems, Internet of Things (IoT), artificial intelligence hardware, and high-performance computing. He highlighted the relevance of Sustainable Development Goal (SDG) 9 – Industry, Innovation and Infrastructure, emphasizing how semiconductor technologies drive innovation, industrial development, and the advancement of modern infrastructure.

Mr. Harish also provided guidance on career planning, skill development, internships, certifications, and higher education opportunities related to VLSI and semiconductor engineering. He encouraged students to undertake practical projects, participate in technical competitions, and stay updated with industry trends to enhance their employability and professional growth.

The session was highly interactive, with students actively participating in discussions and raising questions regarding VLSI design, semiconductor careers, required technical skills, and industry expectations. The speaker addressed these queries with practical examples and valuable career guidance, motivating students to explore opportunities in the semiconductor field.

Overall, the guest lecture was highly informative and enriching. It provided 55 students of III and IV EEE with a comprehensive understanding of VLSI technology, semiconductor industry practices, and career opportunities in the field. The Department of EEE expressed its sincere gratitude to the alumnus for sharing his expertise and professional experiences. Such alumni interactions significantly contribute to enhancing industry

awareness, technical knowledge, and career readiness among students while strengthening alumni engagement and supporting institutional quality initiatives.

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Report on Guest Lecture by Mrs. P. Nagamani



Guest Lecture on "Exploring Career Pathways in Core Engineering Sectors"
by Mrs. P. Nagamani

A guest lecture titled "Exploring Career Pathways in Core Engineering Sectors" was organized on 31/01/2026 for the students of III B. Tech, Department of Electrical and Electronics Engineering through the online mode. The session was delivered by Mrs. P. Nagamani (2014–2018 Batch EEE Alumni), currently working as an R & D Engineer at ABB Global Industries & Services Pvt. Ltd., Hyderabad. The objective of the lecture was to create awareness among students about career opportunities available in core engineering sectors and to guide them in aligning their skills and career aspirations with industry requirements.

During the session, the speaker highlighted the significance of core engineering disciplines in driving technological advancements and industrial development. She provided an overview of various sectors relevant to electrical and electronics engineering graduates, including power systems, renewable energy, industrial automation, electrical machines, control systems, research and development, manufacturing, and smart grid technologies. The speaker emphasized the growing demand for skilled engineers capable of addressing modern industrial challenges.

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Mrs. P. Nagamani shared her professional journey from academia to her current role in research and development, providing valuable insights into the practical aspects of working in a multinational engineering organization. She explained the responsibilities of R&D engineers, including product development, testing, innovation, problem-solving, and continuous improvement of engineering solutions. Through real-world examples, she demonstrated how engineering concepts learned in classrooms are applied to solve industrial problems and develop advanced technologies.

The speaker also discussed the skills and competencies required to build a successful career in core engineering sectors. She emphasized the importance of strong technical fundamentals, analytical thinking, communication skills, teamwork, and continuous learning. Students were encouraged to enhance their employability through internships, industrial training, technical certifications, project-based learning, and participation in professional activities. She also highlighted the importance of staying updated with emerging technologies such as Industry 4.0, digitalization, renewable energy systems, and automation.

In addition, the session focused on the relevance of Sustainable Development Goal (SDG) 8 – Decent Work and Economic Growth, highlighting how skilled engineers contribute to industrial productivity, innovation, sustainable economic growth, and the creation of quality employment opportunities. The speaker encouraged students to develop a professional mindset and pursue careers that contribute positively to society and economic development.

Mrs. Nagamani further provided guidance on higher education opportunities, competitive examinations, recruitment processes, and strategies for preparing for interviews and placements in core engineering companies. She motivated students to identify their strengths, set clear career goals, and continuously upgrade their knowledge and skills to remain competitive in the evolving job market.

The session was highly interactive, with students actively participating and seeking clarification on career planning, skill development, higher studies, and opportunities in core industries. The speaker addressed these queries with practical suggestions and valuable insights from her professional experience, inspiring students to confidently pursue careers in engineering domains.

Overall, the guest lecture was highly informative and motivating. It provided 51 students of III EEE with a clear understanding of career pathways in core engineering sectors, industry expectations, and professional development opportunities. The Department of Electrical and Electronics Engineering expressed its sincere

appreciation to the alumna for sharing her expertise and experiences. Such alumni interactions play a vital role in enhancing student awareness, career preparedness, and industry engagement while supporting institutional quality initiatives and student progression.

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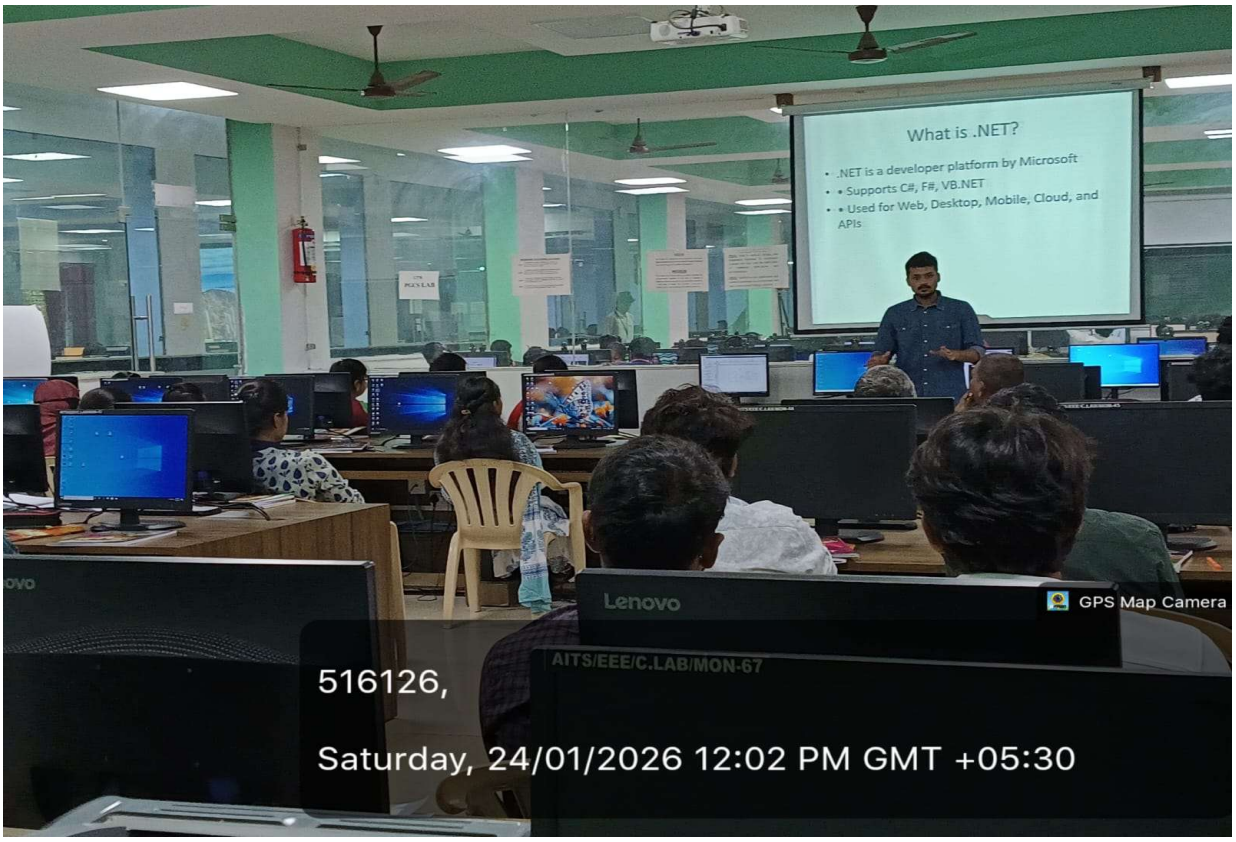
Report on Guest Lecture by Mr. B. Subbarayudu



Guest Lecture on ".NET: Skills, Careers, and Real-World Applications" by B. Subbarayudu

A guest lecture titled **".NET: Skills, Careers, and Real-World Applications"** was organized on 24/01/2026 for the students of III B. Tech, Department of Electrical and Electronics Engineering. The session was delivered by Mr. B. Subbarayudu (2014–2018 Batch EEE Alumni), currently working as a Senior Software Engineer at Capgemini, Hyderabad. The objective of the lecture was to familiarize EEE students with software career opportunities and to create awareness about the relevance of the .NET platform in real-world industrial applications.

During the session, the speaker introduced students to the .NET framework, explaining its architecture, key components, and commonly used technologies such as C#, ASP.NET, and SQL Server. He highlighted how .NET is widely used in developing enterprise-level applications, web services, desktop applications, and cloud-

integrated solutions. The speaker emphasized the growing demand for software professionals with strong programming and problem-solving skills, even among non-computer science graduates.

Mr. B. Subbarayudu also discussed various career pathways for EEE students in the software industry, explaining how electrical engineering graduates can successfully transition into IT roles by acquiring relevant technical skills. He provided guidance on essential competencies such as logical thinking, object-oriented programming concepts, database management, and basic knowledge of web technologies. He further shared insights on industry expectations, recruitment processes, and the importance of internships, certifications, and project-based learning.

In addition, the speaker explained the real-world applications of .NET across different sectors, including banking, healthcare, manufacturing, and automation industries. He encouraged students to work on mini-projects, participate in coding platforms, and build strong resumes highlighting both core engineering knowledge and software skills.

The session was interactive, with students actively asking questions related to skill development, career transition from EEE to software roles, learning resources, and placement preparation. The speaker addressed these queries with practical examples from his professional experience, motivating students to explore software careers with confidence.

Overall, the guest lecture was highly informative and motivating. It provided III EEE students with a clear understanding of .NET technologies, software career opportunities, and real-world applications relevant to the industry. The department expressed its appreciation to the alumnus for sharing valuable insights, as such sessions significantly contribute to student career awareness, employability enhancement, and institutional quality benchmarks related to alumni engagement and student progression.

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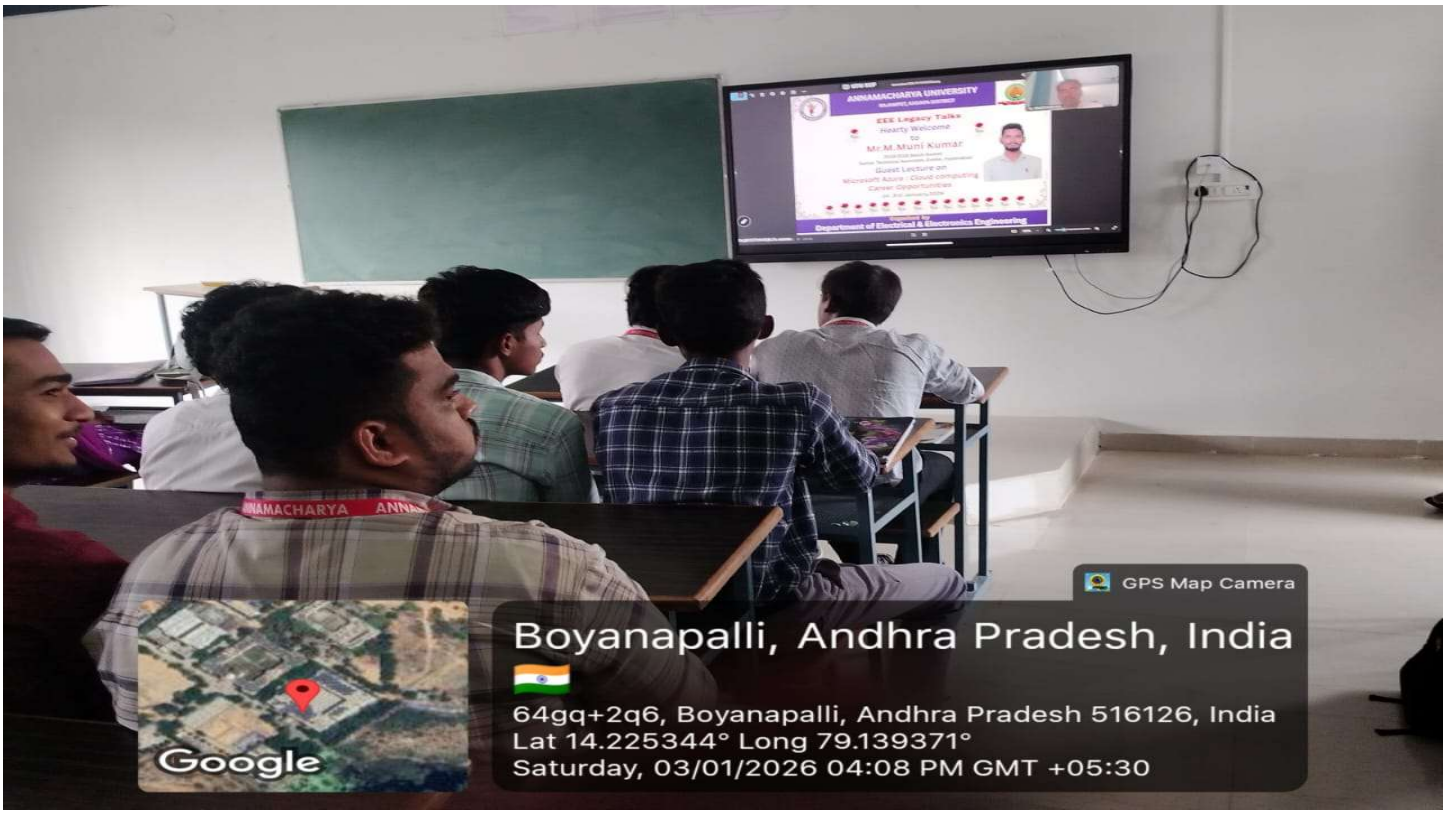
Report on Guest Lecture by Mr. M. Muni Kumar



Guest Lecture on "Microsoft Azure: Cloud Computing, Career Opportunities" by M. Muni Kumar

A guest lecture (online) titled **"Microsoft Azure: Cloud Computing, Career Opportunities"** was organized on 03/01/2026 for the students of III / IV B. Tech, Department of Electrical and Electronics Engineering. The session was delivered by **Mr. M. Muni Kumar (2014–2018 Batch EEE Alumni), currently working as a Senior Technical Associate at Evoke, Hyderabad**. The objective of the lecture was to introduce students to cloud computing concepts, familiarize them with the Microsoft Azure platform, and create awareness about emerging career opportunities in the cloud domain for engineering graduates.

During the session, the speaker explained the fundamentals of cloud computing, including service models such as IaaS, PaaS, and SaaS, and deployment models like public, private, and hybrid clouds. He provided a clear overview of the Microsoft Azure ecosystem, highlighting key services such as virtual machines, storage, networking, databases, and security features. The speaker emphasized how cloud platforms like Azure are increasingly being adopted across industries for scalable, reliable, and cost-effective solutions.

Mr. Muni Kumar also discussed various career opportunities associated with Microsoft Azure, including roles such as cloud engineer, Azure administrator, cloud support associate, DevOps engineer, and solutions architect. He guided students on the skill sets required, importance of hands-on practice, and the role of Azure certifications in improving employability. He also shared insights on how students from EEE background can transition into cloud and IT roles by building foundational knowledge in networking, operating systems, and basic programming.

In addition, the speaker shared practical guidance on career preparation, advising students to work on mini-projects, use Azure free-tier resources for practice, participate in online labs, and follow structured learning paths. He highlighted the importance of continuous learning, adaptability, and aligning technical skills with current industry requirements.

The session was interactive, with students actively participating and asking questions related to cloud platforms, certification pathways, job roles, and future scope in the IT and cloud computing sectors. The speaker addressed these queries with real-time industry experiences and practical advice, motivating students to explore cloud-based career opportunities confidently.

Overall, the guest lecture was highly informative and beneficial. It provided students with a clear understanding of Microsoft Azure, cloud computing concepts, and associated career prospects. The department expressed its appreciation to the alumnus for sharing valuable knowledge and industry insights, as such interactions play a vital role in enhancing student awareness, employability, and meeting institutional quality benchmarks related to student progression and alumni engagement.

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Report on Guest Lecture by Mr. A. Leelendra Prasad



Guest Lecture on "Beyond the GATE: Unlocking Your Engineering Future" by A. Leelendra Prasad

A guest lecture titled **"Beyond the GATE: Unlocking Your Engineering Future"** was organized on 20/12/2025 for the students of the Department of Electrical and Electronics Engineering. The session was delivered by **Mr. A. Leelendra Prasad (2019–2023 Batch Alumni), currently pursuing M. Tech in Power Engineering at IIT Kanpur.** The objective of the lecture was to create awareness among engineering students about opportunities available beyond merely qualifying the GATE examination and to guide them in planning a successful engineering career.

During the session, the speaker explained the significance of the GATE examination not only as a gateway for higher studies but also as a foundation for diverse career opportunities. He discussed various pathways such as M. Tech and Ph.D. programs at premier institutes (IITs, IISc, NITs), research careers, public sector undertakings (PSUs), and interdisciplinary domains where a strong GATE score plays a crucial role. The speaker emphasized the importance of selecting specialization areas based on individual interests and long-term career goals.

Mr. Leelendra Prasad also shared practical insights into effective GATE preparation strategies, stressing the need for strong conceptual understanding of core subjects, regular problem-solving practice, and time management. He highlighted the role of standard textbooks, previous years' question papers, mock tests, and consistent revision in achieving a good GATE score. In addition, he encouraged students to focus on research-oriented thinking, project work, internships, and technical skill enhancement to unlock opportunities beyond traditional job roles.

The session was interactive, with students actively engaging in discussions and seeking clarification on higher education options, research opportunities, specialization choices, and preparation timelines. The speaker addressed these queries with real-life experiences and practical advice, motivating students to explore broader engineering prospects beyond conventional career paths.

Overall, the guest lecture was highly informative and inspiring. It provided students with a clear understanding of how GATE can serve as a stepping stone toward advanced studies, research, and professional growth. The department expressed its appreciation to the alumnus for sharing valuable insights, as such sessions significantly contribute to student career development and support institutional quality benchmarks related to student progression and alumni engagement.

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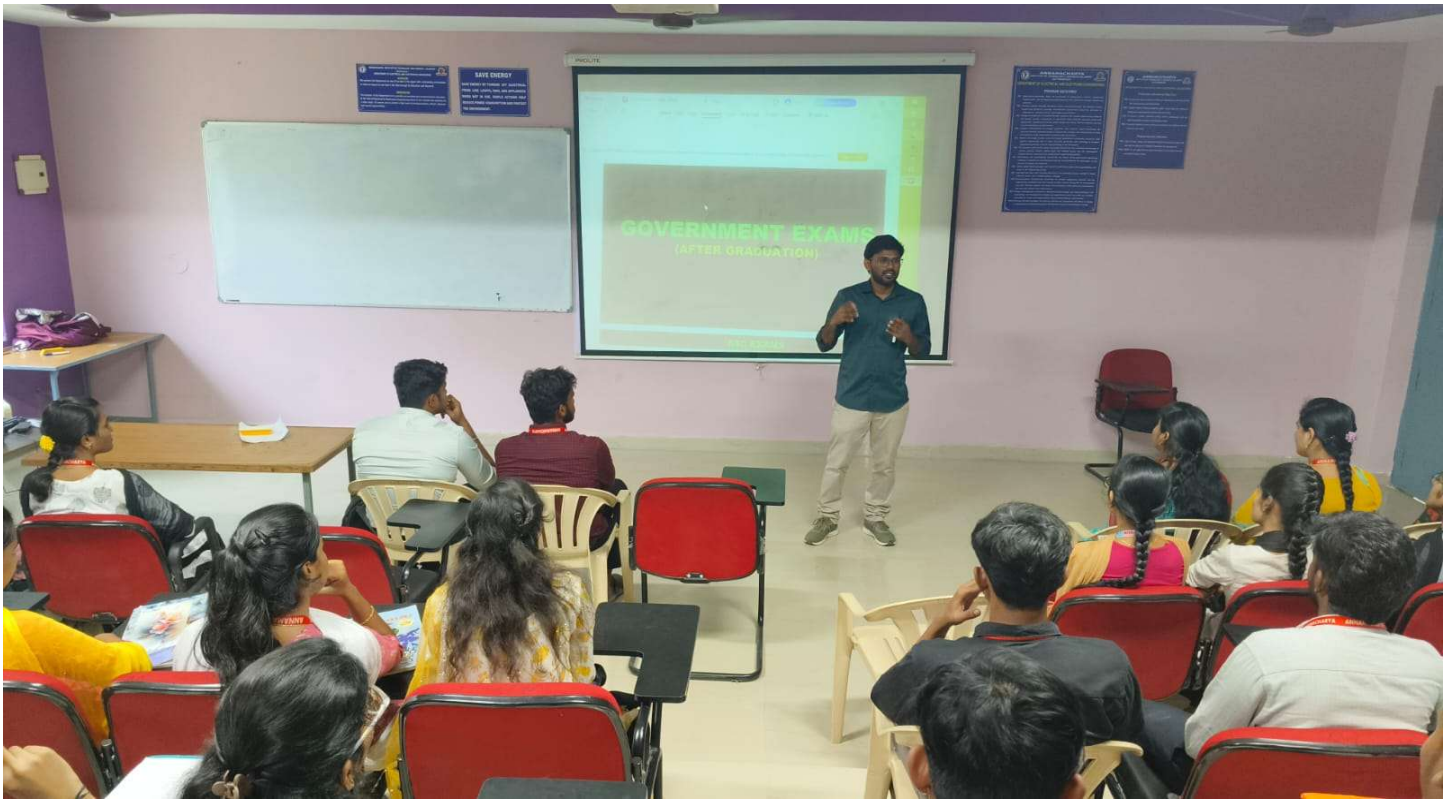
Report on Guest Lecture by P. Sreenivasula Reddy



A guest lecture on **"Career Horizons: Exploring the Avenues for Graduates"** by **Mr. P. Sreenivasula Reddy, (2015-2019 batch EEE Alumni), Social Security Officer, Ministry of Labour and Employment, Govt. of India, Chennai**, was organized on 11/10/2025 for B. Tech EEE students. The session focused on creating awareness among EEE students about various **government job opportunities**, particularly examinations conducted by **Staff Selection Commission (SSC)**, **Railway Recruitment Board (RRB)**, and other public sector organizations.

During the lecture, the resource person provided detailed information on career opportunities available for EEE graduates in government sectors such as **SSC JE, SSC CGL, RRB JE, RRB SSE, State Electricity Boards, and Public Sector Undertakings (PSUs)**. The eligibility criteria, selection process, job profiles, pay structure, and career growth associated with these examinations were clearly explained. The speaker emphasized how core electrical engineering subjects such as **Electrical Machines, Power Systems, Power Electronics, Control Systems, and Measurements** play a vital role in clearing these competitive exams.

The speaker also guided students on effective **preparation strategies**, highlighting the importance of conceptual clarity, regular practice, and time management. Standard textbooks, online platforms, and mock test series were recommended to help students prepare systematically. The importance of **aptitude, reasoning, and general awareness** for exams like SSC was also stressed, along with tips on balancing academic coursework and competitive exam preparation.

The session was highly interactive, with students actively participating and raising questions regarding exam patterns, preparation timelines, and career stability in government jobs. The speaker addressed these queries with practical insights, motivating students to plan their preparation early and stay consistent in their efforts.

Overall, the guest lecture provided valuable guidance to EEE students by exposing them to **secure and prestigious career opportunities in government services**. The department appreciated the contribution of the guest speaker, as such sessions play an important role in guiding students toward structured career planning and fulfilling quality benchmarks related to student progression and employability.